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11-20-06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.09/388,857
Filing Date September 1, 1999
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L.M. Schillinger
Attorney's Docket No.MI22-878
Customer No.021567
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

**RESPONSE TO OCTOBER 11, 2006 NOTICE OF ALLOWANCE
TO ACCOMPANY REQUEST FOR CONTINUED EXAMINATION (RCE)**

To: Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
Tel. 509-624-4276; Fax 509-838-3424
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated October 11, 2006, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.